

Micro Commercial Components 20736 Marilla Street Chatsworth CA 91311

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DTC114TUA

Features

- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0 and MSL Rating 1
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit)
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects
- Only the on/off conditions need to be set for operation, making device design easy

Absolute Maximum Ratings

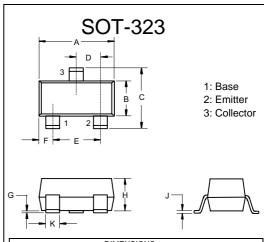
ABOUTATO MAXIMUM NATURES					
Parameter	Symbol	Value	Unit		
Collector-Base Voltage	V _{CBO}	50	V		
Collector-Emitter Voltage	V _{CEO}	50	V		
Emitter-Base voltage	V _{EBO}	5	V		
Collector Current-Continuous	I _C	100	mA		
Collector Dissipation	Pc	200	mW		
Junction Temperature	TJ	150	$^{\circ}\!\mathbb{C}$		
Storage Temperature Range	T _{STG}	-55~150	°C		

Electrical Characteristics

Sym	Parameter	Min	Тур	Max	Unit
$V_{(BR)CBO}$	Collector-Base Breakdown Voltage (I _C =50uA, I _E =0)	50			V
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage (I _C =1mA, I _B =0)	50			٧
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage (I _E =50uA, I _C =0)	5			V
I _{CBO}	Collector Cut-off Current $(V_{CB}=50V, I_E=0)$			0.5	uA
I _{EBO}	Emitter Cut-off Current (V _{EB} =4V, I _C =0)			0.5	uA
h _{FE}	DC Current Gain (V _{CE} =5V, I _C =1mA)	100	300	600	
$V_{\text{CE(sat)}}$	Collector-Emitter Saturation Voltage (I _C =10mA, I _B =1mA)			0.3	V
R₁	Input Resistor	7	10	13	ΚΩ
f⊤	Transition Frequency (V _{CE} =10V, I _C =-5mA, f=100MHz)		250		MHz

*Marking: 04

NPN Digital Transistor



INCHES			
INCHES		MM	
MAX	MIN	MAX	NOTE
.087	1.80	2.20	
.053	1.15	1.35	
.087	2.00	2.20	
.026 Nominal		0.65Nominal	
.055	1.20	1.40	
.016	.30	.40	
.004	.000	.100	
.039	.90	1.00	
.010	.100	.250	
.016	.30	.40	
,	MAX .087 .053 .087 .087 .087 .087 .016 .004 .039 .010	MAX MIN	MAX MIN MAX 0.87 1.80 2.20 .053 1.15 1.35 .087 2.00 2.20 .055 1.20 1.40 .016 .004 .000 .100 .039 .90 1.00 .010 .010 .010 .010 .010 .050 .010 .010 .050 .010 .010 .250

Suggested Solder Pad Layout 0.70 1.90 1.90 0.65 0.65

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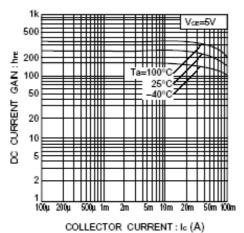


Fig.1 DC current gain vs. collector current

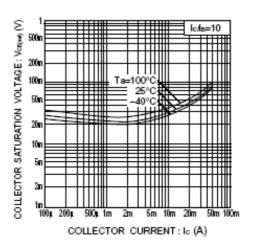
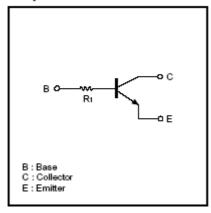


Fig.2 Collector-emitter saturation voltage vs. collector current

●Equivalent circuit





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Ordering Information

Device	Packing
(Part Number)-TP	Tape&Reel3Kpcs/Reel

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